



# 200630801 EFR32FG22 EFR32BG22C112 Full Production Data Sheet Rev 1.0 and Errata

**Bulletin Issue Date:** 6/30/2020

**Effective Date:** 6/30/2020

## Description of Change

Silicon Labs is pleased to announce available of Data Sheet Rev 1.0 for the EFR32FG22 and EFR32BG22C112 Wireless Gecko SoC families. All OPNs have achieved Full Production (FP). The Errata have been updated to Rev 0.4 for EFR32FG22 and Rev 0.2 for EFR32BG22C112.

Notable updates in the Data sheet:

- Finalized remaining MIN / MAX specifications according to characterization and qualification results.
- Updated DCDC lifetime condition guidance in 4.4.1 DC-DC Operating Limits.
- Expanded IADC descriptions to include information at higher oversampling ratios and added typical performance curve

Notable new Errata:

- RADIO\_E303 – RAIL packet filters work incorrectly when header is enabled (EFR32FG22 only)
- USART\_E302 — Additional SCLK Pulses are generated in the USART Synchronous Mode

## Reason for Change

Updated Data Sheet and Errata

## Product Identification

Existing Part #  
EFR32BG22C112F352GM32-C  
EFR32FG22C121F512GM32-C  
EFR32FG22C121F512GM40-C

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

Please contact your local Silicon Labs sales representative with any questions about this notification. A list of Silicon Labs sales representatives may be found at <http://www.silabs.com>.

## Customer Actions Needed:

Review updated Data Sheet and Errata documents.

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